

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	388	banks and shar\$3 with address adj bus	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/13 16:47
L2	137	banks and shar\$3 with address adj bus with data adj bus	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/13 16:48
L3	7	2 and bank with address adj register	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/13 16:48
L4	6	2 and bank with data adj register	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/13 16:52
L5	1	4 and address with match with detect\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/13 16:53
L6	3551	365/230.03.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/13 16:53
L7	11	6 and address adj match\$3 adj detect\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/13 16:55
L8	0	control adj circuit.clm. with write adj data adj held.clm. with data adj register adj as adj read adj data. clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/13 16:56